

DELHI TECHNOLOGICAL UNIVERSITY

Established by Govt. of Delhi vide Act 6 of 2009 (FORMERLY DELHI COLLEGE OF ENGINEERING) BAWANA ROAD, DELHI-110042

Dated: 25.10.2017

No. F.DTU/Rectt./Prof. /Ass.Prof./2017

RECRUITMENT TO FACULTY POSITIONS

Delhi Technological University is a non-affiliating; teaching cum research University engaged in education, research, technology incubation, product innovation and extension work in Applied Sciences, Engineering, Technology, Management and allied areas and is committed to foster excellence. The University invites applications from the talented individuals to fill up the posts of Professors and Associate Professors on regular basis in the disciplines of Computer Engineering, Information Technology, Software Engineering, Electronics & Communication Engineering, Electrical Engineering and Mathematics & Computing by direct mode of recruitment:-

Vacancy position of Professor*

S. No.	Name of Discipline		Category			Total No.
		UR	OBC	SC	ST	of posts
1	Computer Engineering	02	01	01	01	05
2	Information Technology	02	01	01	-	04
3	Software Engineering	01	01	01		03
4	Electronics & Comm. Engg.	01	01	01	01	04
5	Electrical Engineering	02	01	01	01	05
6	Mathematics & Computing	01	01	01		03
	Total No. of posts	09	06	06	03	24

Vacancy position of Associate Professor*

S. No.	Name of Discipline		Category			Total No.
		UR	OBC	SC	ST	of posts
1	Computer Engineering	05	02	02	02	11
2	Information Technology	02	01		01	04
3	Software Engineering	03	01	01		05
4	Electronics & Comm. Engg.	03	02		01	06
5	Electrical Engineering	02	02	01	01	06
6	Mathematics & Computing	02	01	01	01	05
·	Total No. of posts	17	09	05	06	37

^{*}The number of posts advertised is provisional and can be varied as per the requirement of the University.

Important Dates:

Last date of receipt of applications:

18.12.2017

Date of Presentation & Interview will be notified within due course of time. The candidates are advised to keep on watching the University website for further updates.

Note:

- 1. The candidates are required to fill up the online application form and also send the print out of the online application form alongwith the desired/relevant documents.
- 2. The application fee will be amounting to Rs.1,000/- in case of General/OBC candidates and Rs.500/- in case of SC/ST/PH Category candidates. The application fee will be acceptable through online mode only. No other mode of payment will be accepted by the University. The candidates are directed to attach the print out of online fee receipt with hard copy of the Application Form.
- 3. The hard copy of the Application Form alongwith supporting documents should reach to the Registrar, Delhi Technological University before the last date of receipt of applications, i.e., 18.12.2017.
- 4. Candidates will be informed through e-mail for presentation & interview and the details will also be uploaded on the University website.
- 5. Selected candidates will be required to join on short notice.

PAY SCALE, ESSENTIAL QUALIFICATIONS, RELEVANT BRANCH, AGE LIMIT AND RELAXATIONS FOR THE POST OF PROFESSOR IN DISCIPLINES OF COMPUTER ENGINEERING, SOFTWARE ENGINEERING & INFORMATION TECHNOLOGY

Designation, Pay Band and Academic Grade Pay (AGP)	Essential Qualifications	Relevant Branch	Experience	Age Limit
Professor with AGP Rs. 10000/- in PB - IV (Rs. 37,400/- -67000/-)	I. B.E./B.Tech and M.E./ M.Tech in relevant branch with 1st class or equivalent either in B.E./B.Tech or M.E./ M.Tech from a recognized University OR' 1st class MCA and 1st class in M.Tech in relevant branch from a recognized University	Advanced Communication and Information System, Advanced Electronics & Communication Engineering, Artificial Intelligence, Computer & Communication Engineering, Computer Applications, Computer Engineering, Computer Engineering & Applications, Computer Networking, Computer Science, Computer Science & Engineering, Computer Science & Information Technology, Computer Technology & Applications, Computer Science and Systems Engineering, Computer Technology, Electrical & Electronics Engineering, Electrical Engineering, Electronic & Computer Engineering,	Minimum of 10 years teaching/ research/ industrial experience of which at least 5 years should be at the level of Associate Professor or Minimum of 13 years experience in teaching and / or Research and/or Industry.	'Relaxations' 1. The age is relaxable for SC/ST/PH candidates upto 5 years and for OBC candidates upto 3 years in respect of vacancies reserved for them respectively.

	Electronic Engineering,		
	Electronic & Communication		
'OR'	Engineering,	In case of research	2. Relaxable
I st class or equivalent		experience, good	for
in B.E./B.Tech in	Electronics & Telecommunication	academic record	Government
relevant branch/I st	Engineering,	and	Servants
class in MCA and	Information & Communication	books/research	upto 5 years
Ph.D in relevant	Technology,	paper	in
branch from a	Information Engineering,	publications/	accordance
recognized	Information Science & Engineering,	IPR/patents record	with the
University.	Information Science & Engineering,	shall be required	instructions
II. Qualifications as	Information Security,	as deemed fit by	or orders
above with PhD or	Information Systems,	_	issued by the
equivalent, in relevant	Information Technology,	the expert members of the	Central
branch.	Information Technology,	selection	Government
branch.		committee.	Government
	Engineering,	committee.	
	Mathematics & Computing,		
	Mobile & Pervasive Computing,		
TTT A	Software Engineering,	TC 41	2 D 1 11
III. A minimum score	Software Systems,	If the experience	3. Relaxable
as stipulated in the	Software Technology,	in industry is	for teachers
Academic	Software Testing,	considered, the	of
Performance (ADI)	VLSI Design,	same shall be at	government
Indicator (API)	Web Designing,	managerial level	funded
based on	Web Technologies,	equivalent to	institutions
Performance	3-D Animation & Graphics,	Associate	of higher
Based Appraisal	Applied Electronics &	Professor with	education
System (PBAS),	Instrumentation,	active	for 5 years.
set out by the	Microelectronics.	participation	
AICTE/UGC.		record in devising/	
		designing,	
		planning,	
		executing,	
		analyzing, quality	
		control,	
		innovating,	
		training, technical	
		books/research	
		paper	
		publications/	
		IPR/patents, etc.,	
		as deemed fit by	
		the expert	
		members of the	

Selection committee.

PAY SCALE, ESSENTIAL QUALIFICATIONS, RELEVANT BRANCH, AGE LIMIT AND RELAXATIONS FOR THE POST OF PROFESSOR IN DISCIPLINE OF ELECTRONICS & COMMUNICATION ENGINEERING

Designation, Pay Band and Academic Grade Pay (AGP)	Essential Qualifications	Relevant Branch	Experience	Age Limit
Professor with AGP Rs. 10000/- in PB - IV (Rs. 37,400/- -67000/-)	I. B.E./B.Tech and M.E./ M.Tech in relevant branch with 1st class or equivalent either in B.E./B.Tech or M.E./ M.Tech from a recognized University. 'OR' Ist class or equivalent in B.E./B.Tech in relevant branch and Ph.D. in relevant branch from a recognized University. 'OR' M.Sc. in Physics/Relevant branch and ME/M.Tech. in relevant branch and Ph.D in relevant branch with Ist class or equivalent either in M.Sc. or M.E./M.Tech from a recognized University. II. Qualifications as above with PhD or equivalent, in relevant branch. III. A minimum score as stipulated in the Academic Performance Indicator (API) based Performance Based Appraisal System (PBAS), set out by the AICTE/UGC.	Advanced Electronics, Advanced Electronics & Communication Engineering, Applied Electronics & Instrumentation Engineering, Applied Electronics & Communications, Advanced Communication & Information System, Advanced Computer Aided Design, Biomedical Electronics, Biomedical Signal Processing, Computer Engineering, Computer Engineering & Application, Communication & Signal Processing, Computer & Communication Engineering, Computer Applications, Computer Engineering & Applications, Computer Engineering & Applications, Computer Science & Engineering, Computer Science & Engineering, Communication & Information Systems, Communication & Networking, Communication Engineering & Signal Processing, Communication Engineering & Signal Processing, Communication Networks, Communication Networks, Communication Systems, Digital Electronics & Microprocessor, Digital Electronics & Communication Engineering, Digital Electronics Engineering, Digital Electronics Engineering,	Minimum of 10 years teaching/research/industrial experience of which at least 5 years should be at the level of Associate Professor or Minimum of 13 years experience in teaching and / or Research and/or Industry. In case of research experience, good academic record and books/research paper publications/IPR/patent s record shall be required as deemed fit by the expert members of the selection committee. If the experience in industry is considered, the same shall be at managerial level equivalent to Associate Professor with active participation record in devising/designing, planning, executing, analyzing, quality control, innovating, training, technical books/research paper publications/IPR/patent s, etc., as deemed fit by the expert members of the Selection committee.	'Relaxations' 1. The age is relaxable for SC/ST/PH candidates upto 5 years and for OBC candidates upto 3 years in respect of vacancies reserved for them respectively. 2. Relaxable for Government Servants upto 5 years in accordance with the instructions or orders issued by the Central Government 3. Relaxable for teachers of government funded institutions of higher education for 5 years.

Digital Image Processing, Digital Signal Processing, Digital Systems, Digital Communication, Digital Communication Engineering, Digital Communications, Digital Communications & Networking, Digital Systems & Computer Electronics, Electronic Engineering, Electronics & Communication Engg. Electronics & Computer Science, Electronics (Fiber Optics), Electronics (Robotics). Electronics & Biomedical Engineering, Electronics & Communication Engineering (Microwaves), Electronics & Communications Engineering, Electronics & Computer Engineering, Electronics & Control Systems, Electronics & Electrical Engineering, Electronics & Electrical Communication Engineering, Electronics & Telecommunications Engineering, Electronics & Telematics Engineering, Electronics Design Technology, Electronics Engineering, Electronics Engineering (Industry Integrated), Electronics Engineering (Micro Electronics), Electronics Engineering, (Specialization In Consumer Electronics), Electronics Engineering With Microprocessor, Electrical Engineering, Electronics System Engineering, Electronics Technology, Embedded System & Computing, Embedded System & VLSI, Embedded System & VLSI Design, Embedded Systems,

Embedded Systems Technologies, Image Processing, Industrial Electronics, Integrated Circuits Technology, Integrated Electronics & Circuits, IC Design, Information Technology, Information Science & Engineering, Information Science & Technology, Information Security, Information Systems, Information Technology & Engineering, Mobile & Pervasive Computing, Medical Electronics, Medical Electronics Engineering, Micro & Nano Electronics, Micro Electronics, Micro Electronics & VLSI Design, Micro Electronics & Control Systems, Micro Electronics Engineering, Microelectronics & VLSI Design, Microelectronics Engineering, Mobile Technology, Microwave & Optical Communication, Microwave & Communication Engineering, Microwave & Millimeter Engineering, Microwave & Radar Engineering, Microwave & TV Engineering, Microwave Engineering, Microwaves, Microwave & Optical Communication, Mobile Communication, Mobile Communication & Network Technology, Modern Communication Engineering, Nano Science & Technology, Nano Electronics, Nano Technology, Optics & Optoelectronics, Opto Electronics & Communication Systems,

Optoelectronics & Communication, Opto-Electronics Engineering, Optoelectronics - Optical Communication, Optical Communication, Radar & Communication, Radio Frequency & Microwave Engineering, Radar & Satellite Communication, Radio Physics & Electronics, RF & Photonics, Signal Processing, Signal Processing & Digital Design, Signal Processing & Communications, Signal Processing & Embedded Systems, Telecommunication Engineering, VLSI, VLSI Design, VLSI & Embedded Systems, VLSI & Embedded Systems Design, VLSI And Microelectronics, VLSI Design & Embedded Systems, VLSI Design & Signal Processing, VLSI Design & Testing, VLSI System Design, VLSI Systems, VLSI Design Tools & Technology, Wireless & Mobile Communications, Wireless Sensor Networks. Wireless Communication & Computing, Wireless Communication Technology, Wireless Communications, Wireless Networks & Applications, Instrumentation Engineering, Instrumentation & Control Engineering, Power Electronics.

PAY SCALE, ESSENTIAL QUALIFICATIONS, RELEVANT BRANCH, AGE LIMIT AND RELAXATIONS FOR THE POST OF PROFESSOR IN THE DISCIPLINE OF ELECTRICAL ENGINEERING

Designation, Pay Band and	Essential Qualifications	Relevant Branch	Experience	Age Limit
Academic Grade Pay (AGP)	Quantications			
(AGP) Professor with AGP Rs. 10000/- in PB - IV (Rs. 37,400/67000/-)	I. B.E./B.Tech and M.E./ M.Tech in relevant branch with 1st class or equivalent either in B.E./B.Tech or M.E./ M.Tech from a recognized University. 'OR' Ist Class or equivalent in B.E./B.Tech in relevant branch and Ph.D. in relevant branch from a recognized University. II. Qualifications as above with PhD or equivalent, in relevant branch. III. A minimum score as stipulated in the Academic Performance Indicator (API) based on Performance Based Appraisal System (PBAS), set out by the AICTE/UGC.	Signal Processing & Embedded System, Process Control, Control Engineering, Measurement & Instrumentation, Digital Design, Microelectronics & VLSI Design, RF and Microwave Engineering, Telecommunication Systems	Minimum of 10 years teaching/research/industrial experience of which at least 5 years should be at the level of Associate Professor or Minimum of 13 years experience in teaching and / or Research and/or Industry. In case of research experience, good academic record and books/research paper publications/IPR/paten ts record shall be required as deemed fit by the expert members of the selection committee. If the experience in industry is considered, the same shall be at managerial level equivalent to Associate Professor with active participation record in devising/designing, planning, executing, analyzing, quality control, innovating,	50 years 'Relaxations' 1. The age is relaxable for SC/ST/PH candidates upto 5 years and for OBC candidates upto 3 years in respect of vacancies reserved for them respectively. 2. Relaxable for Government Servants upto 5 years in accordance with the instructions or orders issued by the Central Government 3. Relaxable for teachers of government funded institutions of higher education for 5 years.
	AICIE/UGC.	Engineering, Power & Energy Systems, Machine Drives & Power Electronics, Robotics System, Communication Engineering, Control & Computing,	control, innovating, training, technical books/research paper publications/IPR/paten ts, etc., as deemed fit by the expert members of the Selection committee.	

Power Electronics & Power Systems, Electronics Systems, Power & Control, Signal Processing, Signal Processing & Digital Design, Machine Drives & Power Electronics. Power & Energy Systems Engineering, Instrumentation & Signal processing, Advance Communication & Information System, Advanced Electrical Power System, Advanced Electronics. Advanced Electronics & Communication Engineering, Applied Electronics, Applied Electronics & Communications System, Applied Instrumentation, Automation & Control Power Systems, Bio Electronics, Biomedical Electronics, Biomedical Signal Processing & Instrumentation, Communication Engineering & Signal Processing, Computer Applications In Industrial Drives, Control Engineering, Digital Communication, Digital Communication & Networking, Digital Electronics, Digital Electronics & Communication Engineering, Digital Electronics & Engineering, Digital Image processing, Digital Instrumentation, Digital Signal Processing, Digital Systems, Digital Systems & Communication, Electric Power System, Electrical Drive & Power Engineering, Electrical & Power Engineering, Electrical Energy Systems, Electrical Engg (Instrumentation & Control),

Electrical Instrumentation & Control Engineering, Electrical Power & Energy Systems, Electrical Power Systems, Electronics Circuits & System Design, Electronics & Communication (VLSI Design), Electronics & Instrumentation Engineering, Electronic & Tele communication Engineering, Electronic & Control Systems, Electronics & Telecommunication Engg (Radio & Systems), Electronics Communication & Instrumentation Engineering, Electronics Design & Technology, Electronics Product Design & Technology, Electronics Systems & Communication, Electronics Technology, Electronics Tele Communication, Embedded & Real Time Systems, Embedded Systems & VLSI Design, Embedded Systems, **Embedded Systems** Technologies, Energy Engineering, Guidance & Navigation Control, Guided Missiles, High Voltage & Power System Engineering, Illumination Engineering, Illumination Technology & Design Image Processing, Industrial Automation & RF Engineering, Industrial drives & Control, Industrial Electronics, Industrial Power Control & Drives. Instrumentation Engineering, Integrated Circuits Technology, Integrated Power Systems, Micro & Nano Electronics, Micro Electronics & VLSI deigns, Micro Electronics & Control Systems. Micro Electronics Engineering,

Microwave & Optical Communication Engineering, Microwave & Communication Engineering, Microwave & Millimeter Engineering, Microwave & Radar Engg, Microwave & TV Engineering, Microwave Engineering, Optics & Optoelectronics, Optoelectronics & Communication, Optoelectronics & Laser Technology, Optoelectronics Engineering, Power & Energy Engineering, Power & Industrial Drives, Power Control & drives. Power Electronics & Control, Power Electronics & Electrical Drives, Power Electronics & Machine Drives, Power Electronics & Systems, Power Electronics Engineering, Power Engineering & Energy Systems, Power system & Control, Power System & Control Automation, Power System with Emphasis on H.V. Engineering, Power Systems & Automation, Power Systems & Power Electronics, Power Systems Control & Automation Engineering, Radio Physics & Electronics, Reliability Engineering, Renewable Energy, Sensor Technology, Signal Processing & Communication, Solar Power Systems, Telecommunication Engineering, Telematics, VLSI & Embedded Systems Design, VLSI & Microelectronics, VLSI Design, VLSI Design & Embedded Systems, VLSI Design & Signal Processing,

VLSI Design & Testing, VLSI System Design, VLSI Systems, Applied Electronics & Instrumentation Engineering, Biomedical Engineering, Biomedical Instrumentation, Electrical & Electronics (Power System), Electrical & Instrumentation Engineering, Electrical & Power Engineering, Electrical Engineering (Electronics & Power), Electrical Engineering Industrial Control, Electrical Instrumentation & Control Engineering, Electrical, Electronics & Power Electronics Science & Engineering, Electronic Instrumentation & Control Engineering, Electronics & Telecommunication Engineering, Electronics & Computer Engineering, Electronics & Control Systems, Electronics & Electrical Engineering, Electronics & Power Engineering, Electronics System Engineering, Information Technology & Engineering, Instrument Technology, Instrumentation & Electronics, Mechatronics Engineering, Medical Electronics Engineering, Power Electronics & Instrumentation Engineering, Energy & Environment

Management.

PAY SCALE, ESSENTIAL QUALIFICATIONS, RELEVANT BRANCH, AGE LIMIT AND RELAXATIONS FOR THE POST OF PROFESSOR IN THE DISCIPLINE OF MATHEMATICS & COMPUTING

Designation, Pay Band and	Essential Qualifications	Relevant Branch	Experience	Age Limit
Academic Academic	Quantications			
Grade Pay				
(AGP)				
Professor	I. B.E./B.Tech and	Mathematics,	Minimum of 10 years	50 years
with AGP	M.E./ M.Tech in	Applied Mathematics,	teaching/research/industrial	(D. 1
Rs. 10000/-	relevant branch	Statistics,	experience of which at least	'Relaxations'
in PB - IV	with 1 st class or	Mathematical Statistics,	5 years should be at the	1. The age is
(Rs. 37,400/- -67000/-)	equivalent either in B.E./B.Tech or	Applied Statistics, Operation Research,	level of Associate Professor or Minimum of 13 years	relaxable for SC/ST/PH
-07000/-)	M.E./ M.Tech	Mathematics &	experience in teaching and /	candidates upto
	from a recognized	Computing,	or Research and/or	5 years and for
	University	Mathematics & Computer	Industry.	OBC
		Applications,		candidates upto
	'OR'	Financial Mathematics,	In case of research	3 years in
	Ist class or	Computer Science,	experience, good academic	respect of
	equivalent in	Computer Engineering,	record and books/research	vacancies
	B.E./B.Tech in	Computer Science & Engineering,	paper	reserved for them
	relevant branch and Ph.D. in	Computer Technology,	publications/IPR/patents record shall be required as	respectively.
	relevant branch	Computer Technology, Computer Applications,	deemed fit by the expert	2. Relaxable for
	from a recognized	Computer Engineering &	members of the selection	Government
	University	Applications,	committee.	Servants upto 5
		Computer Science &		years in
	'OR'	Technology,	If the experience in industry	accordance
	M.Sc./M.A. in	Computer Technology &	is considered, the same	with the
	relevant branch	Applications.	shall be at managerial level	instructions or
	with at least 55% marks (or an		equivalent to Associate Professor with active	orders issued by the Central
	equivalent grade in		participation record in	Government.
	a point scale		devising/designing,	3. Relaxable for
	wherever grading		planning, executing,	teachers of
	system is		analyzing, quality control,	government
	followed) and		innovating, training,	funded
	Ph.D in relevant		technical books/research	institutions of
	branch.		paper	higher
	II. Qualifications as above with PhD or		publications/IPR/patents, etc., as deemed fit by the	education for 5 years.
	equivalent, in		expert members of the	years.
	relevant branch.		Selection committee.	
	III. A minimum			
	score as stipulated			
	in the Academic			
	Performance			
	Indicator (API)			
	based on Performance			
	Based Appraisal			
	System (PBAS),			
	set out by the			
	AICTE/UGC.			

PAY SCALE, ESSENTIAL QUALIFICATIONS, RELEVANT BRANCH, AGE LIMIT AND RELAXATIONS FOR THE POST OF ASSOCIATE PROFESSOR IN DISCIPLINES OF COMPUTER ENGINEERING, SOFTWARE ENGINEERING & INFORMATION TECHNOLOGY

Designation,	Essential Qualifications	Relevant Branch	Experience	Age Limit
Pay Band and				
Academic				
Grade Pay				
(AGP)				
Associate	I. B.E./B.Tech and M.E./	Advanced Communication and	Minimum of 5 years	45 years
Professor	M.Tech in relevant	Information System,	experience in	
with AGP	branch with 1 st class or	Advanced Electronics &	teaching/research	'Relaxations'
Rs. 9000/- in	equivalent either in	Communication Engineering,	/industry at the level	1. The age is
PB - IV	B.E./B.Tech or M.E./	Artificial Intelligence,	of Assistant	relaxable for
(Rs. 37,400/-	M.Tech from a	Computer & Communication	Professor or	SC/ST/PH
-67000/-)	recognized University	Engineering,	equivalent.	candidates
		Computer Applications,		upto 5 years
		Computer Engineering,		and for OBC
		Computer Engineering &	In case of research	candidates
	'OR'	Applications,	experience, good	upto 3 years in
		Computer Networking,	academic record and	respect of
	1 st class MCA and 1 st	Computer Science,	books/research paper	vacancies
	class in M.Tech in	Computer Science &	publications/IPR/pate	reserved for
	relevant branch from	Engineering,	nts record shall be	them
	a recognized	Computer Science &	required as deemed	respectively.
	University	Information Technology,	fit by the expert	
		Computer Technology &	members of the	
	'OR'	Applications,	selection committee.	
		Computer Science &		2. Relaxable for
	I st class or equivalent	Technology,		Government
	in B.E./B.Tech in	Computer Science and Systems	If the experience in	Servants upto 5
	relevant branch/I st	Engineering,	industry is	years in
	class in MCA and	Computer Technology,	considered, the same	accordance
	Ph.D in relevant	Electrical & Electronics	shall be at managerial	with the
	branch from a	Engineering,	level equivalent to	instructions or
	recognized	Electrical Engineering,	Assistant Professor	orders issued
	University.	Electronic & Computer	with active	by the Central
		Engineering,	participation record	Government
	II. Qualifications as	Electronic Engineering,	in devising/designing,	3. Relaxable for
	above with PhD or	Electronic & Communication	planning, executing,	teachers of
	equivalent, in relevant	Engineering,	analyzing, quality	government
	branch.	Electronics & Instrumentation,	control, innovating,	funded
		Electronics &	training, technical	institutions of
	III. A minimum score	Telecommunication	books/research paper	higher
	as stipulated in the	Engineering,	publications/IPR/pate	education for 5
	Academic	Information & Communication	nts, etc., as deemed	years.
	Performance	Technology,	fit by the expert	
	Indicator (API)	Information Engineering,	members of the	
	based on	Information Science &	Selection committee.	
	Performance	Engineering,		
	Based Appraisal	Information Science &		
	System (PBAS),	Technology,		
	set out by the	Information Security,		
	AICTE/UGC.	Information Systems,		
		Information Technology,		
		Information Technology &		
		Engineering,		

	Mathematics & Computing,		
	Mobile & Pervasive		
	Computing,		
	Software Engineering,		
	Software Systems,		
	Software Technology,		
	Software Testing,		
	VLSI Design,		
	Web Designing,		
	Web Technologies,		
	3-D Animation & Graphics,		
	Applied Electronics &		
	Instrumentation,		
	Microelectronics.		

PAY SCALE, ESSENTIAL QUALIFICATIONS, RELEVANT BRANCH, AGE LIMIT AND RELAXATIONS FOR THE POST OF ASSOCIATE PROFESSOR IN DISCIPLINE OF ELECTRONICS & COMMUNICATION ENGINEERING

Designation,	Essential Qualifications	Relevant Branch	Experience	Age Limit
Pay Band and				
Academic				
Grade Pay				
(AGP)				
Associate	I. B.E./B.Tech and	Advanced Electronics,	Minimum of 5 years	45 years
Professor	M.E./ M.Tech in	Advanced Electronics &	experience in	
with AGP	relevant branch with	Communication Engineering,	teaching/research	'Relaxations'
Rs. 9000/- in	1 st class or equivalent	Applied Electronics,	/industry at the level	
PB - IV	either in B.E./B.Tech	Applied Electronics &	of Assistant Professor	1. The age is
(Rs. 37,400/-	or M.E./ M.Tech	Instrumentation Engineering,	or equivalent.	relaxable for
-67000/-)	from a recognized	Applied Electronics &		SC/ST/PH
	University.	Communications,		candidates
		Advanced Communication &	In case of research	upto 5 years
	'OR'	Information System,	experience, good	and for OBC
		Advanced Computer Aided	academic record and	candidates
	Ist class or equivalent	Design,	books/research paper	upto 3 years
	in B.E./B.Tech in	Biomedical Electronics,	publications/IPR/paten	in respect of
	relevant branch and	Biomedical Signal Processing,	ts record shall be	vacancies
	Ph.D. in relevant	Computer Engineering,	required as deemed fit	reserved for
	branch from a	Computer Engineering &	by the expert members	them
	recognized	Application,	of the selection	respectively.
	University.	Communication & Signal	committee.	2. Relaxable
		Processing,		for
	'OR'	Computer & Communication		Government
	M.Sc. in	Engineering,	If the experience in	Servants
	Physics/Relevant	Computer Applications,	industry is considered,	upto 5 years
	branch and	Computer Engineering,	the same shall be at	in
	ME/M.Tech. in	Computer Engineering &	managerial level	accordance
	relevant branch and	Applications,	equivalent to Assistant	with the
	Ph.D in relevant	Computer Science &	Professor with active	instructions
	branch with Ist class	Engineering,	participation record in	or orders
	or equivalent either in	Computer Science &	devising/designing,	issued by the
	M.Sc. or	Technology,	planning, executing,	Central
	M.E./M.Tech from a	Communication & Information	analyzing, quality	Government
	recognized	Systems,	control, innovating,	
	University.		training, technical	

- II. Qualifications above with PhD or equivalent, in relevant branch.
- III. A minimum score as stipulated in the Academic Performance Indicator (API) based Performance Based Appraisal System (PBAS), set out by the AICTE/UGC.
- Communication & Networking, Communication Engineering, Communication Engineering & Signal Processing, Communication Networks, Communication Systems, Digital Design, Digital Electronics,
- Digital Electronics & Microprocessor,
- Digital Electronics & Communication,
- Digital Electronics & Communication Engineering,
- Digital Electronics & Communication Systems,
- Digital Electronics
- Engineering,
- Digital Image Processing,
- Digital Signal Processing,
- Digital Systems,
- Digital Communication,
- Digital Communication
- Engineering,
- Digital Communications,
- Digital Communications &
- Networking,
- Digital Systems & Computer
- Electronics,
- Electronic Engineering,
- Electronics & Communication
- Engg,
- Electronics & Computer
- Science,
- Electronics (Fiber Optics),
- Electronics (Robotics),
- Electronics & Biomedical
- Engineering,
- Electronics & Communication
- Engineering (Microwaves),
- Electronics &
- Communications Engineering,
- Electronics & Computer
- Engineering,
- Electronics & Control
- Systems,
- Electronics & Electrical
- Engineering,
- Electronics & Electrical
- Communication Engineering,
- Electronics &
- Telecommunications
- Engineering,
- Electronics & Telematics
- Engineering,

books/research paper publications/IPR/paten ts, etc., as deemed fit by the expert members of the Selection committee.

Relaxable for teachers of government funded institutions higher of education for 5 years.

Electronics Design Technology, Electronics Engineering, **Electronics Engineering** (Industry Integrated), Electronics Engineering (Micro Electronics), Electronics Engineering, (Specialization In Consumer Electronics), Electronics Engineering With Microprocessor, Electrical Engineering, Electronics System Engineering, Electronics Technology, Embedded System & Computing. Embedded System & VLSI, Embedded System & VLSI Design, Embedded Systems, Embedded Systems Technologies, Image Processing, Industrial Electronics, **Integrated Circuits** Technology, Integrated Electronics & Circuits, IC Design, Information Technology, Information Science & Engineering, Information Science & Technology, Information Security, Information Systems, Information Technology & Engineering, Mobile & Pervasive Computing, Medical Electronics. Medical Electronics Engineering, Micro & Nano Electronics, Micro Electronics, Micro Electronics & VLSI Design, Micro Electronics & Control Systems, Micro Electronics Engineering, Microelectronics & VLSI Design, Microelectronics Engineering, Mobile Technology,

Microwave & Optical Communication, Microwave & Communication Engineering, Microwave & Millimeter Engineering, Microwave & Radar Engineering, Microwave & TV Engineering, Microwave Engineering, Microwaves, Microwave & Optical Communication, Mobile Communication, Mobile Communication & Network Technology, Modern Communication Engineering, Nano Science & Technology, Nano Electronics, Nano Technology, Optics & Optoelectronics, Opto Electronics & Communication Systems, Optoelectronics & Communication, Opto-Electronics Engineering, Optoelectronics - Optical Communication, Optical Communication, Radar & Communication, Radio Frequency & Microwave Engineering, Radar & Satellite Communication, Radio Physics & Electronics, RF & Photonics, Signal Processing, Signal Processing & Digital Design, Signal Processing & Communications, Signal Processing & Embedded Systems, Telecommunication Engineering, VLSI, VLSI Design, VLSI & Embedded Systems, VLSI & Embedded Systems Design, VLSI And Microelectronics, VLSI Design & Embedded Systems, VLSI Design & Signal

Processing,

	VLSI Design & Testing,	
	VLSI System Design,	
	VLSI Systems,	
	VLSI Design Tools &	
	Technology,	
	Wireless & Mobile	
	Communications,	
	Wireless Sensor Networks,	
	Wireless Communication &	
	Computing,	
	Wireless Communication	
	Technology,	
	Wireless Communications,	
	Wireless Networks &	
	Applications,	
	Instrumentation Engineering,	
	Instrumentation & Control	
	Engineering,	
	Power Electronics.	

PAY SCALE, ESSENTIAL QUALIFICATIONS, RELEVANT BRANCH, AGE LIMIT AND RELAXATIONS FOR THE POST OF ASSOCIATE PROFESSOR IN THE DISCIPLINE OF ELECTRICAL ENGINEERING

Designation,	Essential Qualifications	Relevant Branch	Experience	Age Limit
Pay Band and				
Academic				
Grade Pay				
(AGP)				
Associate	I. B.E./B.Tech and	Electrical Engineering,		45 years
Professor	M.E./ M.Tech in	Electrical & Electronics		
with AGP	relevant branch	Engineering,	Minimum of 5 years	'Relaxations'
Rs. 9000/- in	with 1 st class or	Electronics Engineering,	experience in	
PB - IV	equivalent either in	Electronics & Communication	teaching/research	
(Rs. 37,400/-	B.E./B.Tech or	Engineering,	/industry at the level	1. The age is
-67000/-)	M.E./ M.Tech from	Electronics & Electrical	of Assistant	relaxable for
	a recognized	Communication Engineering,	Professor or	SC/ST/PH
	University.	Instrumentation & Control	equivalent.	candidates
		Engineering,		upto 5 years
	'OR'	Control & Instrumentation,		and for OBC
		Power Engineering,		candidates
	Ist Class or	Electronics & Applied	In case of research	upto 3 years
	equivalent in	Instrumentation Engineering,	experience, good	in respect of
	B.E./B.Tech in	Instrumentation Engineering,	academic record and	vacancies
	relevant branch and	High Voltage Engineering,	books/research paper	reserved for
	Ph.D. in relevant	Electrical Machine & Drives,	publications/IPR/pate	them
	branch from a	Drive & Power Electronics,	nts record shall be	respectively.
	recognized	Power Systems,	required as deemed	
	University.	Power Electronics & Drives,	fit by the expert	
		Power Apparatus & Systems,	members of the	
		Electrical Machines,	selection committee.	
		Power Apparatus & Electric		
		Drives,		
		Systems & Control,		
		System Engineering,		

- II. Qualifications as above with PhD or equivalent, in relevant branch.
- III. A minimum score as stipulated in the Academic Performance (API) Indicator based Performance Based Appraisal System (PBAS), set out by the AICTE/UGC.
- Energy Systems, Microwave & Optical Communication, Communication Systems, Signal Processing & Embedded System, Process Control, Control Engineering, Measurement & Instrumentation, Digital Design, Microelectronics & VLSI Design, RF and Microwave Engineering, **Telecommunication Systems** Engineering,

Control & Computing,

Electronics Systems, Power & Control, Signal Processing,

Systems,

Design,

Electronics,

Engineering,

processing,

System,

Systems,

Bio Electronics,

Instrumentation,

Signal Processing,

Industrial Drives, Control Engineering, Digital Communication,

Biomedical Electronics,

Biomedical Signal Processing &

Communication Engineering &

Computer Applications In

Power Electronics & Power

Signal Processing & Digital

Machine Drives & Power

Power & Energy Systems

Instrumentation & Signal

Information System, Advanced Electrical Power

Advanced Electronics, Advanced Electronics & Communication Engineering,

Applied Electronics, Applied Electronics & Communications System, Applied Instrumentation, Automation & Control Power

Advance Communication &

industry is considered, the same shall be at managerial level equivalent to Assistant Professor with active participation record in devising/designing, planning, executing, analyzing, quality control, innovating, training, technical books/research paper publications/IPR/pate Power & Energy Systems, nts, etc., as deemed Machine Drives & Power fit by the expert members of Electronics. the Robotics System, Selection committee. Communication Engineering,

If the experience in

- Relaxable for Government Servants upto 5 years in accordance with the instructions or orders issued by the Central Government
- Relaxable for teachers of government funded institutions of higher education for 5 years.

Digital Communication & Networking, Digital Electronics, Digital Electronics & Communication Engineering, Digital Electronics & Engineering, Digital Image processing, Digital Instrumentation, Digital Signal Processing, Digital Systems, Digital Systems & Communication, Electric Power System, Electrical Drive & Power Engineering, Electrical & Power Engineering, Electrical Energy Systems, Electrical Engg (Instrumentation & Control), Electrical Instrumentation & Control Engineering, Electrical Power & Energy Systems, Electrical Power Systems, Electronics Circuits & System Design, Electronics & Communication (VLSI Design), Electronics & Instrumentation Engineering, Electronic & Tele communication Engineering, Electronic & Control Systems, Electronics & Telecommunication Engg (Radio & Systems), Electronics Communication & Instrumentation Engineering, Electronics Design & Technology, Electronics Product Design & Technology, Electronics Systems & Communication, Electronics Technology, Electronics Tele Communication, Embedded & Real Time Systems. Embedded Systems & VLSI Design, Embedded Systems, **Embedded Systems** Technologies, Energy Engineering, Guidance & Navigation Control,

Guided Missiles, High Voltage & Power System Engineering, Illumination Engineering, Illumination Technology & Design Image Processing, Industrial Automation & RF Engineering, Industrial drives & Control, Industrial Electronics, Industrial Power Control & Drives. Instrumentation Engineering, Integrated Circuits Technology, Integrated Power Systems, Micro & Nano Electronics, Micro Electronics & VLSI deigns, Micro Electronics & Control Systems, Micro Electronics Engineering, Microwave & Optical Communication Engineering, Microwave & Communication Engineering, Microwave & Millimeter Engineering, Microwave & Radar Engg, Microwave & TV Engineering, Microwave Engineering, Optics & Optoelectronics, Optoelectronics & Communication, Optoelectronics & Laser Technology, Optoelectronics Engineering, Power & Energy Engineering, Power & Industrial Drives, Power Control & drives. Power Electronics & Control, Power Electronics & Electrical Drives, Power Electronics & Machine Drives, Power Electronics & Systems, Power Electronics Engineering, Power Engineering & Energy Systems, Power system & Control, Power System & Control Automation, Power System with Emphasis on H.V. Engineering, Power Systems & Automation, Power Systems & Power Electronics,

Power Systems Control & Automation Engineering, Radio Physics & Electronics, Reliability Engineering, Renewable Energy, Sensor Technology, Signal Processing & Communication, Solar Power Systems, Telecommunication Engineering, Telematics, VLSI & Embedded Systems Design, VLSI & Microelectronics, VLSI Design, VLSI Design & Embedded Systems. VLSI Design & Signal Processing, VLSI Design & Testing, VLSI System Design, VLSI Systems, Applied Electronics & Instrumentation Engineering, Biomedical Engineering, Biomedical Instrumentation, Electrical & Electronics (Power System), Electrical & Instrumentation Engineering, Electrical & Power Engineering, **Electrical Engineering** (Electronics & Power), Electrical Engineering Industrial Control, Electrical Instrumentation & Control Engineering, Electrical, Electronics & Power Electronics Science & Engineering, Electronic Instrumentation & Control Engineering, Electronics & Telecommunication Engineering, Electronics & Computer Engineering, Electronics & Control Systems, Electronics & Electrical Engineering, Electronics & Power Engineering, Electronics System Engineering, Information Technology & Engineering, Instrument Technology,

Instrumentation & Electronics,
Mechatronics Engineering,
Medical Electronics
Engineering,
Power Electronics &
Instrumentation Engineering,
Energy & Environment
Management.

PAY SCALE, ESSENTIAL QUALIFICATIONS, RELEVANT BRANCH, AGE LIMIT AND RELAXATIONS FOR THE POST OF ASSOCIATE PROFESSOR IN THE DISCIPLINE OF MATHEMATICS & COMPUTING

Designation,	Essential Qualifications	Relevant Branch	Experience	Age Limit
Pay Band and				
Academic				
Grade Pay				
(AGP)				
Associate	I. B.E./B.Tech and M.E./	Mathematics,	Minimum of 5 years	45 years
Professor	M.Tech in relevant	Applied Mathematics,	experience in	
with AGP	branch with 1 st class or	Statistics,	teaching/research	'Relaxations'
Rs. 9000/- in	equivalent either in	Mathematical Statistics,	/industry at the level	1. The age is
PB - IV	B.E./B.Tech or M.E./	Applied Statistics,	of Assistant	relaxable for
(Rs. 37,400/-	M.Tech from a	Operation Research,	Professor or	SC/ST/PH
-67000/-)	recognized University	Mathematics &	equivalent.	candidates upto
		Computing,		5 years and for
	'OR'	Mathematics & Computer	In case of research	OBC
	Ist class or equivalent	Applications,	experience, good	candidates upto
	in B.E./B.Tech in	Financial Mathematics,	academic record and	3 years in
	relevant branch and	Computer Science,	books/research paper	respect of
	Ph.D. in relevant	Computer Engineering,	publications/IPR/pate	vacancies
	branch from a	Computer Science &	nts record shall be	reserved for
	recognized University	Engineering,	required as deemed	them
	(07)	Computer Technology,	fit by the expert	respectively.
	'OR'	Computer Applications,	members of the	2. Relaxable for
	M.Sc./M.A. in	Computer Engineering &	selection committee.	Government
	relevant branch with	Applications,	TC 41	Servants upto 5
	at least 55% marks (or	Computer Science &	If the experience in	years in
	an equivalent grade in	Technology,	industry is	accordance
	a point scale wherever	Computer Technology &	considered, the same	with the
	grading system is followed) and Ph.D in	Applications.	shall be at managerial	instructions or orders issued
	relevant branch.		level equivalent to Assistant Professor	orders issued by the Central
	II. Qualifications as		with active	Government.
	above with PhD or		participation record	3. Relaxable for
	equivalent, in relevant		in devising/designing,	teachers of
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	III A minimum score		1	
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	* *		Selection committee.	
	AICTE/UGC.			
	Performance Based Appraisal System (PBAS), set out by the		planning, executing, analyzing, quality control, innovating, training, technical books/research paper publications/IPR/pate nts, etc., as deemed fit by the expert members of the	government funded institutions of higher education for 5 years.

Notes:

- 1. Any deviation in the nomenclature of the relevant branches as mentioned above may also be considered by the University.
- 2. AMIE/IETE qualifications in relevant branches mentioned in the RR are also eligible.
- 3. B.Sc. (Engineering), B.E., B.Tech, B.S. (Four years) shall be considered as equivalent.
- 4. M.Sc. (Engineering), M.E., M.Tech, M.S. shall be considered as equivalent.
- 5. Selection Committee, may in cases of exceptional merit, recommend additional increments in case of higher qualifications, experience and academic achievements by the candidates.
- 6. Persons already in employment in Government Department/Autonomous Bodies/Universities under Central/State Government should apply through proper channel.
- 7. If a class/division is not awarded, minimum of 60% marks in aggregate shall be considered equivalent to first class/division. If a Grade Point in 10 point scale system is adopted the Cumulative Grade Point Average will be converted into equivalent marks as below:-

Grade Point	% of Marks
6.25	55%
6.75	60%
7.25	65%
7.75	70%
8.25	75%

General Instructions for all Candidates

- i. The candidate must be a citizen of India.
- ii. Application Form may be downloaded from the University website www.dtu.ac.in
- iii. Application Form must be accompanied by a online fee receipt of Rs.1000/- in case of General/OBC candidates and Rs.500/- in case of SC/ST/PH Category candidates. No other mode of payment will be accepted by the University.
- iv. Application Form must also be accompanied by self attested copies of Educational and Professional Qualifications, Experience, Caste Certificate/PH Certificate (in case claiming benefit of reservation).
- v. Prescribed Educational Qualifications are minimum & mere possession of the same does not entitle candidates to be called for interview, where number of applications received are more, the University reserves the right to short-list the candidates to be called for interview and no claim for refund of fee shall be entertained in any case.
- vi. The University reserves the right to fill or not to fill any or all the post advertised, no correspondence whatsoever will be entertained from the candidates regarding postal delays, conduct and result of interview and reason for not being called for interview.
- vii. The number of posts advertised is provisional and can be varied as per the requirement of the University.
- viii. Reservation benefits will be available to the SC/ST/OBC/PH category candidates in accordance with the instructions / orders / circulars issued from time to time by the Govt. of Delhi. The reservation benefits under SC/STs shall be admissible as per judgment dated 12.9.2012 of Hon'ble High Court of Delhi as passed in No. 5390/2010, CM No. 20815/2010 Deepak Kumar and Ors Vs District and Sessions Judge, Delhi and Ors. as per which SC/ST candidates of other States/UTs shall also be eligible for reservation benefit. However, this shall be further subject to policy decision of Govt. of Delhi for SC/ST migrants of other States.
- ix. The OBC candidates must be in possession of filled prescribed Annexure I, alongwith his/her caste certificate issued by the Govt. of Delhi only.
- x. Abbreviations used are denoted as under: UR-Un-Reserved (General), SC- Scheduled Caste, ST- Schedule Tribe, OBC-Other Backward Classes, PH Physically Handicapped, VH–Visually Handicapped, LV-Low Vision, OH-Orthopedically Handicapped, BL-Both Leg, OA–One Arm, OL–One Leg, OAL-One Arm and One Leg, HH–Hearing Handicapped.
- xi. Application must be sent in the prescribed format only. The candidates applying for more than one discipline or post should submit separate application form alongwith requisite fees.

- xii. The Educational qualification, age, experience and other conditions of eligibility as stipulated above against the post shall be determined as on the closing date of receipt of applications.
- xiii. All columns must be filled in the application form. No column should be left blank, instead it should be marked "N.A." wherever not applicable.
- xiv. Applicants already in employment in Government Department/ Autonomous Bodies/Universities under Central/State Government should apply through proper channel.
- xv. The persons working in private organization, claiming previous experience of working in Government Department/Autonomous Bodies/Universities under Central/State Government on regular basis, should enclose a certificate from that Govt. organization stating no vigilance or disciplinary case was pending or contemplated against them. Failure to provide the same shall make them ineligible for consideration to the post.
- xvi. Incomplete/unsigned application/application without photograph/ application not in prescribed performa and those received after closing date or without requisite fees in the form of demand draft shall be rejected and no claim for refund of fee shall be entertained in any case.
- xvii. Candidates must ensure that their application must reach the University well in time. The University will not be responsible for any postal delay or loss.
- xviii. No TA/DA in connection with the submission of application form or appearing in the interview will be paid to the candidate.
- xix. If the qualification possessed by the candidate is equivalent, then the authority (with number and date) under which it has been so treated must be indicated and its copy may also be attached.
- xx. No documents will be accepted or considered by the University after submission of application form by the candidate and no subsequent request for its change will be considered or granted.
- xxi. In case of any query candidate may enquire at email id <u>recruitment@dtu.ac.in</u> or call at 01127871044-45, Ext. No. 1123.
- xxii. Any dispute with regard to this recruitment will be subject to the Courts/Tribunals having jurisdiction over Delhi.

The candidates are required to fill up the online application form and also send the print out of online filled Application Form with the desired/relevant documents to the Registrar, Delhi Technological University before the last date for receipt of applications, i.e., 18.12.2017. The complete application, in the prescribed form, duly filled in alongwith all the desired documents and requisite fee receipt should be submitted in the University campus at the Reception Counter, Administrative Block on any working day OR sent by post, so as to reach the Registrar, Delhi Technological University, Shahbad Daulatpur, Bawana Road, Delhi-110042 latest by 18.12.2017 at 05.00 p.m. (In case the closing date happens to be a public holiday the next working day will be the last date for receiving of applications).

Registrar